

# Frequency Synthesizer

KSN-1900A-119+

50Ω 1830 to 1900 MHz

## The Big Deal

- Low phase noise and spurious
- Robust design and construction
- Small size 0.80" x 0.58" x 0.15"



CASE STYLE: DK1042

## Product Overview

The KSN-1900A-119+ is a Frequency Synthesizer, designed to operate from 1830 to 1900 MHz for satellite application. The KSN-1900A-119+ is packaged in a metal case (size of 0.80" x 0.58" x 0.15") to shield against unwanted signals and noise.

## Key Features

Feature	Advantages
Low phase noise and spurious: <ul style="list-style-type: none"><li>• Phase Noise: -98 dBc/Hz typ. @ 10 kHz offset</li><li>• Comparison Spurious: -104 dBc typ.</li><li>• Reference Spurious: -102 dBc typ.</li></ul>	Low phase noise and spurious improve system EVM (Error Vector Magnitude).
Robust design and construction	To enhance the robustness of KSN-1900A-119+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.
Small size, 0.80" x 0.58" x 0.15"	The small size enables the KSN-1900A-119+ to be used in compact designs.



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# Frequency Synthesizer

KSN-1900A-119+

50Ω 1830 to 1900 MHz

## Features

- Integrated VCO + PLL
- Low phase noise and spurious
- Robust design and construction
- Low operating voltage (VCC VCO=+5V, VCC PLL=+3V)
- Small size 0.80" x 0.58" x 0.15"



CASE STYLE: DK1042  
PRICE: \$29.95 ea. QTY (1-9)

+ RoHS compliant in accordance with EU Directive (2002/95/EC)

The +Suffix has been added in order to identify RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications.

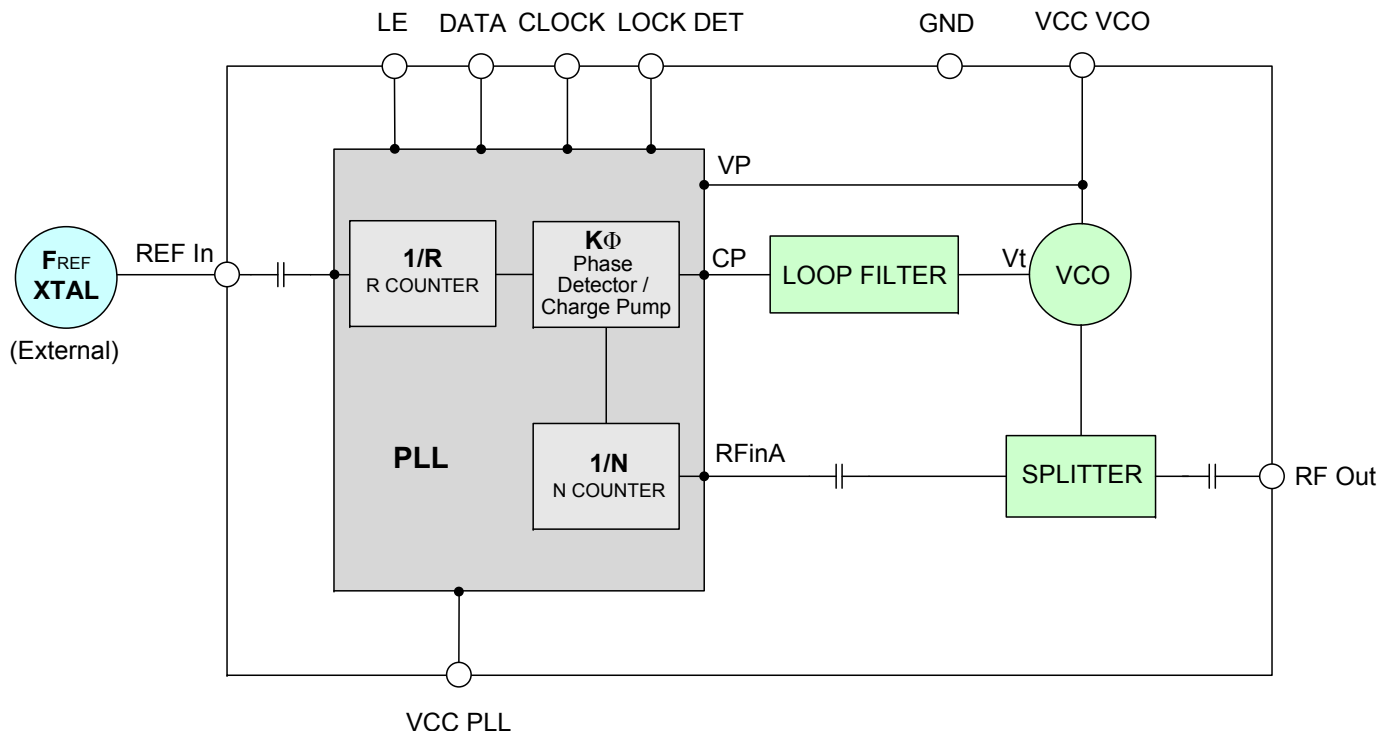
## Applications

- Satellite

## General Description

The KSN-1900A-119+ is a Frequency Synthesizer, designed to operate from 1830 to 1900 MHz for satellite application. The KSN-1900A-119+ is packaged in a metal case (size of 0.80" x 0.58" x 0.15") to shield against unwanted signals and noise. To enhance the robustness of KSN-1900A-119+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.

## Simplified Schematic



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**Electrical Specifications** (over operating temperature -40°C to +85°C)

Parameters	Test Conditions	Min.	Typ.	Max.	Units
Frequency Range	-	1830	-	1900	MHz
Step Size	-	-	1000	-	kHz
Settling Time	Within ± 1 kHz	-	10	-	mSec
Output Power	-	0	+3	+6	dBm
SSB Phase Noise	@ 100 Hz offset	-	-88	-	dBc/Hz
	@ 1 kHz offset	-	-90	-83	
	@ 10 kHz offset	-	-98	-93	
	@ 100 kHz offset	-	-127	-123	
	@ 1 MHz offset	-	-148	-142	
Reference Spurious Suppression	Ref. Freq. 10 MHz	-	-102	-85	dBc
Comparison Spurious Suppression	Step Size 1000 kHz	-	-104	-75	
Non - Harmonic Spurious Suppression	-	-	-90	-	
Harmonic Suppression	-	-	-28	-18	
VCO Supply Voltage	5.00	4.75	5.00	5.25	
PLL Supply Voltage	3.00	2.85	3.00	3.15	V
VCO Supply Current	-	-	46	53	mA
PLL Supply Current	-	-	10	17	
Reference Input (External)	Frequency	10 (square wave)	-	10	MHz
	Amplitude	1	-	1	V <sub>P-P</sub>
	Input impedance	-	-	100	KΩ
	Phase Noise @ 1 kHz offset	-	-	-145	dBc/Hz
RF Output port Impedance	-	-	50	-	Ω
Input Logic Level	Input high voltage	-	2.55	-	V
	Input low voltage	-	-	0.55	V
Digital Lock Detect	Locked	-	2.45	-	V
	Unlocked	-	-	0.40	V
Frequency Synthesizer PLL	-	ADF4106			
PLL Programming	-	3-wire serial 3V CMOS			
Register Map @ 1900 MHz	I_Register	-	(MSB) 01011111100000010010010 (LSB)		
	F_Register	-	(MSB) 01011111100000010010011 (LSB)		
	N_Register	-	(MSB) 001000000111011000110001 (LSB)		
	R_Register	-	(MSB) 00010000000000000101000 (LSB)		

**Absolute Maximum Ratings**

Parameters	Ratings
VCO Supply Voltage	5.8V
PLL Supply Voltage	3.6V
VCO Supply Voltage to PLL Supply Voltage	-0.3V to +5.8V
Reference Frequency Voltage	-0.3Vmin, VCC PLL +0.3Vmax
Data, Clock, LE Levels	-0.3Vmin, VCC PLL +0.3Vmax
Operating Temperature	-40°C to +85°C
Storage Temperature	-55°C to +100°C

Permanent damage may occur if any of these limits are exceeded



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Typical Performance Data

FREQUENCY (MHz)	POWER OUTPUT (dBm)			VCO CURRENT (mA)			PLL CURENT (mA)		
	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
	1830	3.10	3.19	2.99	44.33	46.05	47.19	8.78	9.84
1832	3.09	3.18	2.97	44.33	46.05	47.19	8.79	9.85	11.65
1843	3.04	3.12	2.92	44.30	46.06	47.23	8.77	9.84	11.64
1854	3.01	3.10	2.89	44.28	46.08	47.27	8.81	9.89	11.68
1865	2.98	3.05	2.87	44.28	46.11	47.31	8.80	9.88	11.67
1876	3.01	3.03	2.92	44.31	46.14	47.35	8.79	9.87	11.66
1887	3.04	3.02	2.97	44.35	46.18	47.39	8.83	9.90	11.69
1898	3.00	3.01	2.97	44.39	46.22	47.42	8.82	9.90	11.68
1900	2.99	3.01	2.96	44.39	46.23	47.43	8.82	9.90	11.69

FREQUENCY (MHz)	HARMONICS (dBc)					
	F2			F3		
	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
1830	-28.38	-32.60	-34.92	-23.73	-26.86	-30.11
1832	-28.81	-33.03	-35.15	-23.81	-26.90	-30.22
1843	-31.62	-35.41	-36.50	-24.70	-26.70	-30.61
1854	-33.99	-36.14	-37.02	-26.02	-26.98	-30.91
1865	-36.57	-35.96	-37.23	-25.29	-26.90	-30.65
1876	-37.87	-37.52	-37.13	-24.77	-27.55	-29.84
1887	-39.21	-38.80	-36.74	-26.22	-29.66	-30.11
1898	-41.00	-39.01	-36.56	-27.44	-29.45	-31.01
1900	-41.13	-38.92	-36.46	-27.42	-29.00	-31.09



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FREQUENCY (MHz)	PHASE NOISE (dBc/Hz) @ OFFSETS				
	+25°C				
	100Hz	1kHz	10kHz	100kHz	1MHz
1830	-87.35	-89.38	-98.61	-127.28	-147.85
1832	-86.80	-88.31	-98.45	-127.39	-147.92
1843	-85.52	-89.25	-98.67	-127.45	-146.95
1854	-91.15	-90.18	-98.78	-127.57	-148.39
1865	-86.33	-90.63	-98.77	-127.81	-147.76
1876	-89.38	-89.65	-98.60	-127.86	-148.78
1887	-87.75	-90.93	-98.16	-128.07	-146.99
1898	-84.83	-90.70	-98.78	-128.03	-148.62
1900	-85.89	-90.26	-98.81	-127.88	-148.67

FREQUENCY (MHz)	PHASE NOISE (dBc/Hz) @ OFFSETS				
	-45°C				
	100Hz	1kHz	10kHz	100kHz	1MHz
1830	-85.32	-88.59	-96.55	-127.77	-148.31
1832	-84.67	-90.38	-96.44	-127.67	-148.33
1843	-85.65	-89.08	-96.69	-127.76	-148.52
1854	-88.09	-89.27	-96.89	-127.82	-148.60
1865	-82.06	-88.93	-97.52	-128.07	-148.97
1876	-82.39	-88.41	-97.45	-128.47	-148.46
1887	-86.15	-90.18	-97.73	-128.52	-149.22
1898	-83.29	-88.93	-97.55	-128.54	-149.61
1900	-87.30	-89.77	-97.64	-128.52	-149.70

FREQUENCY (MHz)	PHASE NOISE (dBc/Hz) @ OFFSETS				
	+85°C				
	100Hz	1kHz	10kHz	100kHz	1MHz
1830	-84.38	-85.59	-99.07	-126.42	-147.02
1832	-90.01	-87.36	-98.93	-126.32	-147.09
1843	-86.24	-86.06	-98.71	-126.58	-145.49
1854	-87.47	-88.62	-98.99	-127.17	-146.67
1865	-86.73	-89.13	-98.88	-127.06	-147.36
1876	-84.66	-88.30	-98.46	-127.18	-147.96
1887	-87.21	-89.36	-97.80	-127.35	-147.58
1898	-85.62	-86.05	-98.07	-126.93	-147.69
1900	-87.91	-86.58	-98.20	-126.98	-147.76



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COMPARISON SPURIOUS ORDER	COMPARISON SPURIOUS @Fcarrier 1830MHz+(n*Fcomparison) (dBc) note 1			COMPARISON SPURIOUS @Fcarrier 1865MHz+(n*Fcomparison) (dBc) note 1			COMPARISON SPURIOUS @Fcarrier 1900MHz+(n*Fcomparison) (dBc) note 1			
	n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
	-5	-107.29	-111.02	-113.17	-107.82	-110.19	-101.92	-113.71	-108.57	-103.53
-4	-104.82	-129.59	-130.53	-107.11	-117.22	-103.35	-113.89	-109.82	-104.54	
-3	-104.23	-110.82	-116.80	-106.71	-108.48	-101.49	-108.62	-106.34	-101.93	
-2	-104.16	-105.83	-112.94	-104.33	-112.31	-96.98	-113.03	-107.42	-97.33	
-1	-97.72	-100.32	-106.09	-100.04	-106.12	-90.69	-108.15	-104.75	-90.13	
0 note 2	-	-	-	-	-	-	-	-	-	
+1	-100.99	-104.87	-113.54	-99.56	-115.20	-91.11	-106.87	-107.04	-89.91	
+2	-106.21	-112.07	-125.54	-102.25	-118.43	-98.04	-106.96	-113.64	-96.06	
+3	-106.47	-111.32	-113.01	-105.36	-124.94	-102.30	-108.64	-112.20	-97.74	
+4	-118.64	-118.03	-121.30	-116.66	-121.34	-105.19	-115.82	-117.13	-102.50	
+5	-110.50	-111.23	-113.69	-117.17	-113.92	-106.37	-119.77	-106.13	-105.29	

Note 1: Comparison frequency 1000 kHz  
 Note 2: All spurs are referenced to carrier signal (n=0).

REFERENCE SPURIOUS ORDER	REFERENCE SPURIOUS @Fcarrier 1830MHz+(n*Freference) (dBc) note 3			REFERENCE SPURIOUS @Fcarrier 1865MHz+(n*Freference) (dBc) note 3			REFERENCE SPURIOUS @Fcarrier 1900MHz+(n*Freference) (dBc) note 3			
	n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
	-5	-122.78	-113.20	-116.24	-120.54	-112.99	-110.26	-113.30	-118.89	-113.33
-4	-124.11	-113.27	-118.06	-122.81	-116.67	-113.11	-112.45	-120.97	-113.71	
-3	-124.44	-120.39	-122.11	-130.51	-128.73	-113.18	-114.60	-131.41	-121.74	
-2	-108.99	-104.67	-104.02	-108.26	-105.27	-101.64	-103.17	-106.51	-102.15	
-1	-105.83	-101.16	-103.38	-104.51	-104.97	-98.64	-106.11	-95.85	-97.52	
0 note 4	-	-	-	-	-	-	-	-	-	
+1	-109.08	-99.22	-103.64	-106.21	-108.64	-98.31	-97.60	-96.29	-95.78	
+2	-106.40	-104.47	-104.89	-107.35	-104.39	-103.29	-105.10	-105.40	-102.28	
+3	-115.73	-132.82	-121.61	-116.95	-124.20	-123.26	-120.27	-122.40	-115.50	
+4	-120.77	-131.29	-120.97	-117.60	-131.67	-117.94	-118.72	-121.83	-117.74	
+5	-110.44	-132.49	-120.35	-108.65	-120.01	-117.66	-112.98	-120.37	-119.48	

Note 3: Reference frequency 10 MHz  
 Note 4: All spurs are referenced to carrier signal (n=0).



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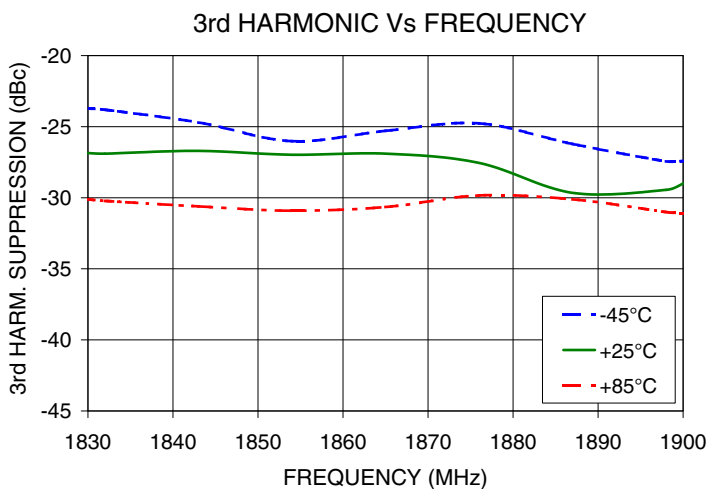
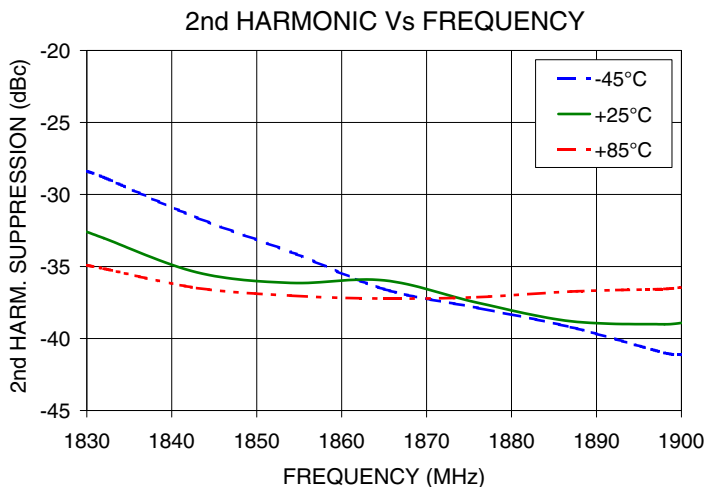
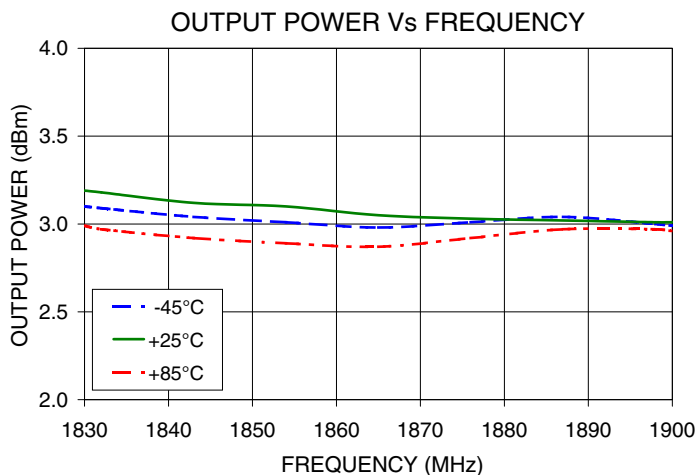


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Typical Performance Curves



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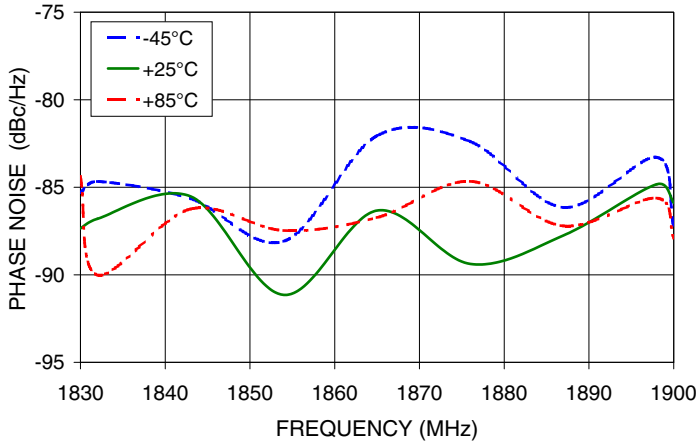


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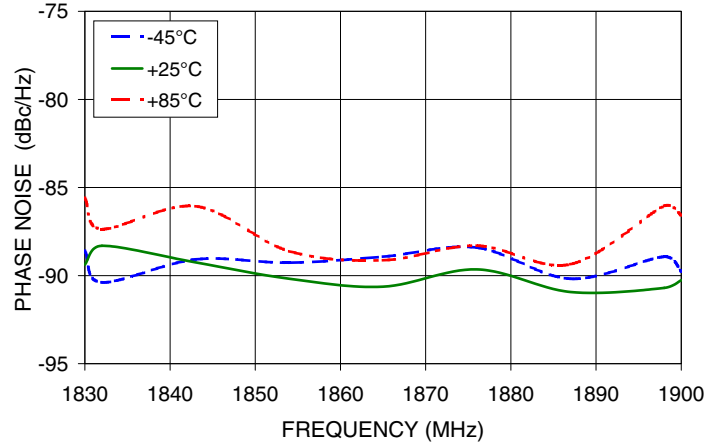


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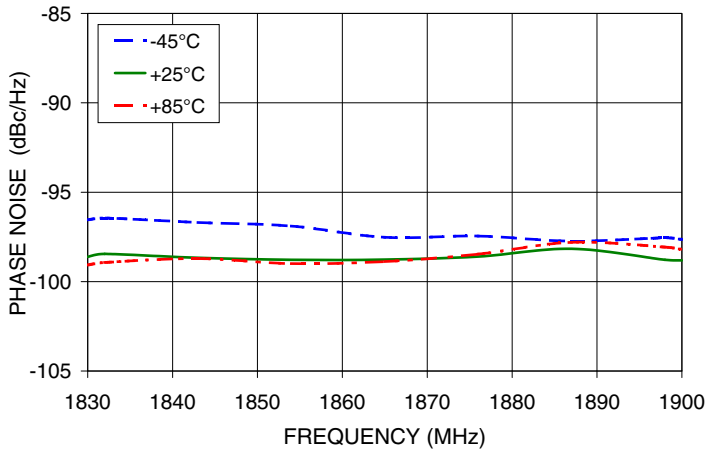
PHASE NOISE @ 100 Hz offset



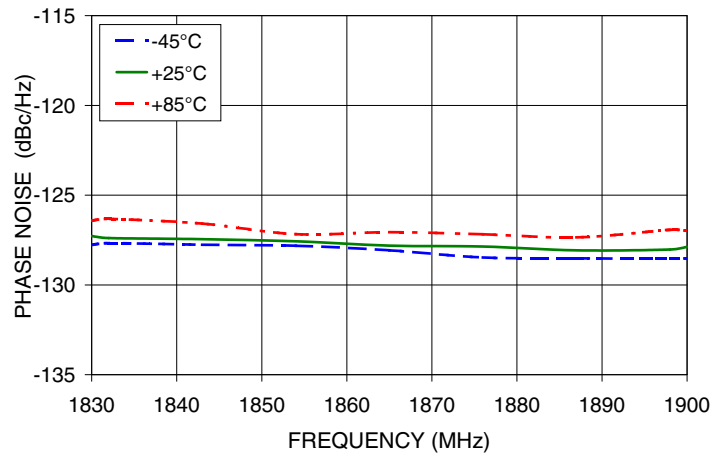
PHASE NOISE @ 1kHz offset



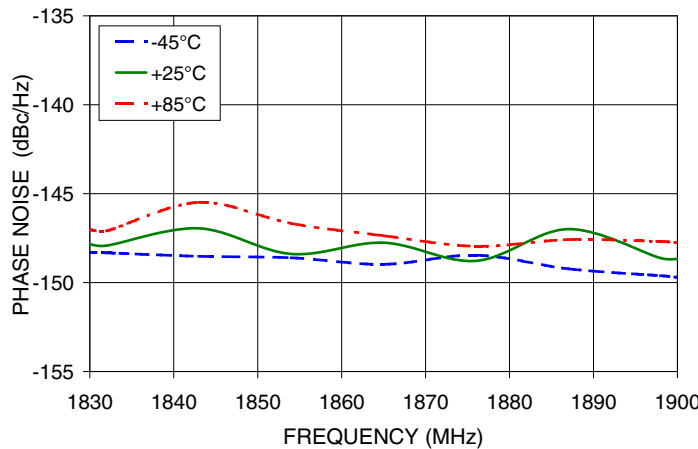
PHASE NOISE @ 10 kHz offset



PHASE NOISE @ 100 kHz offset



PHASE NOISE @ 1MHz offset



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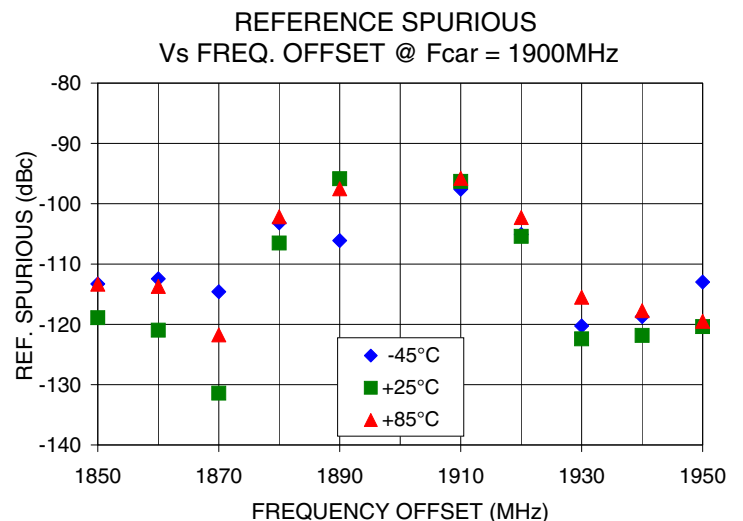
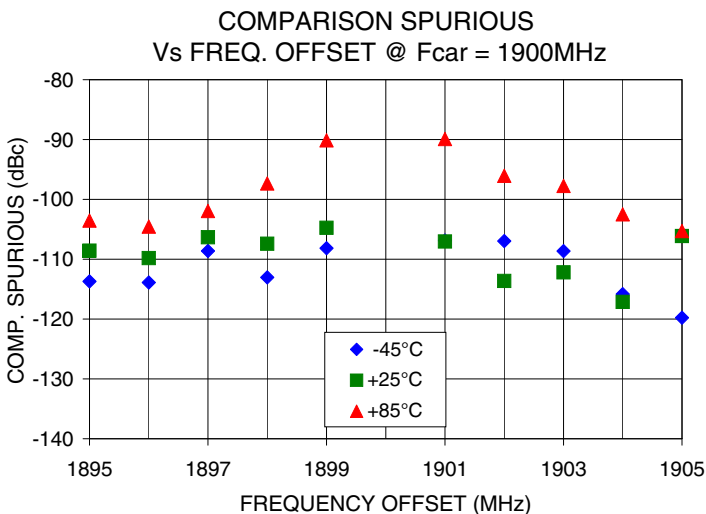
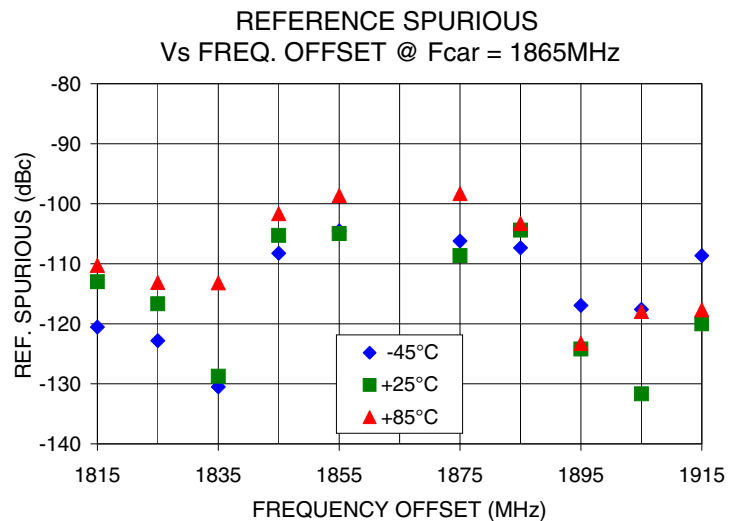
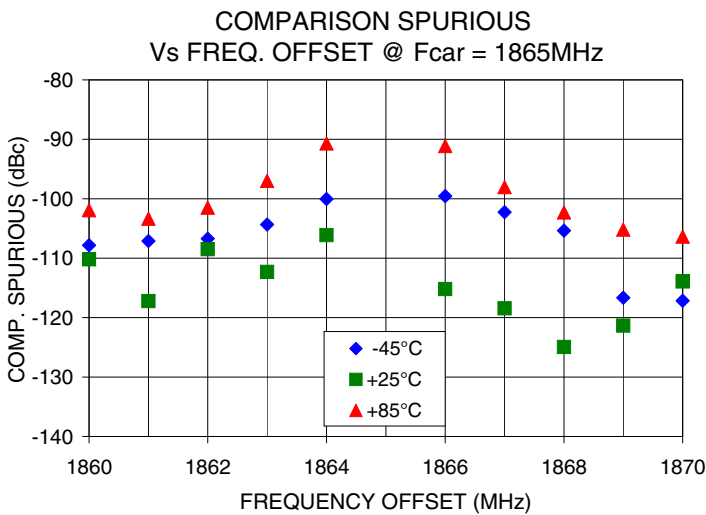
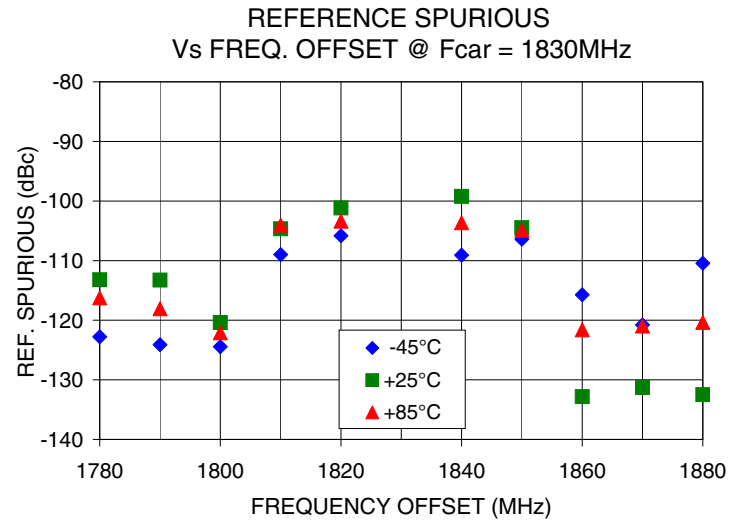
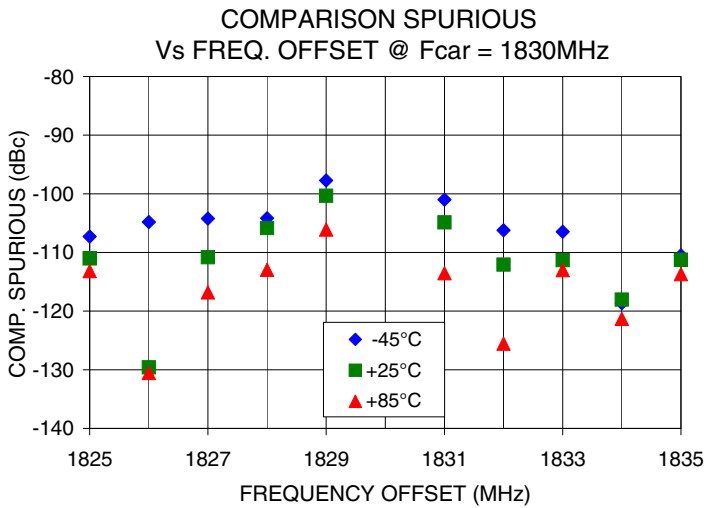


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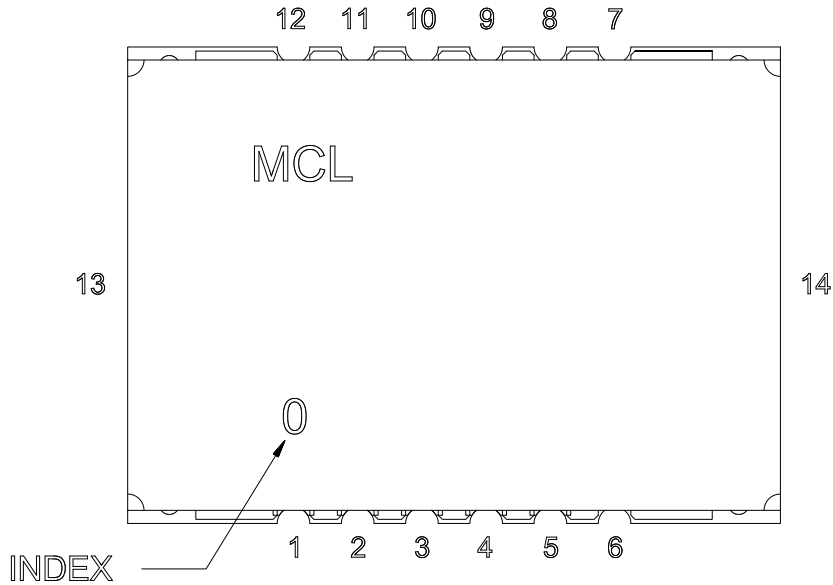


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Notes: 1. Performance and quality attributes and conditions not expressly stated in this specification sheet are intended to be excluded and do not form a part of this specification sheet. 2. Electrical specifications and performance data contained herein are based on Mini-Circuit's applicable established test performance criteria and measurement instructions. 3. The parts covered by this specification sheet are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at [www.minicircuits.com/MCLStore/terms.jsp](http://www.minicircuits.com/MCLStore/terms.jsp).

Pin Configuration

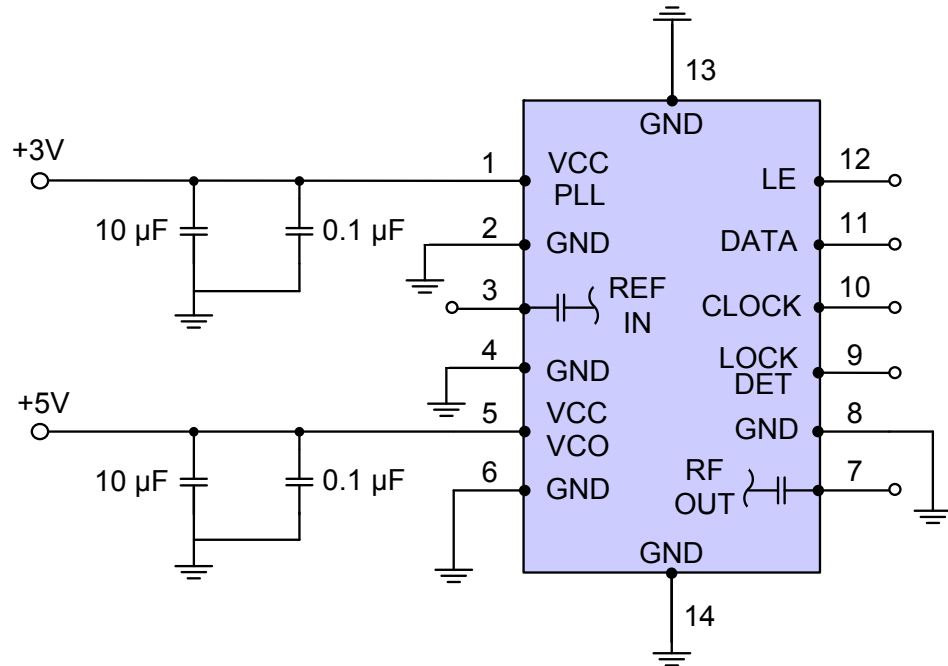


Pin Connection

Pin Number	Function
1	VCC PLL
2	GND
3	REF IN
4	GND
5	VCC VCO
6	GND
7	RF OUT
8	GND
9	LOCK DET
10	CLOCK
11	DATA
12	LE
13	GND
14	GND

Recommended Application Circuit

Note: REF IN and RF OUT ports are internally AC coupled.



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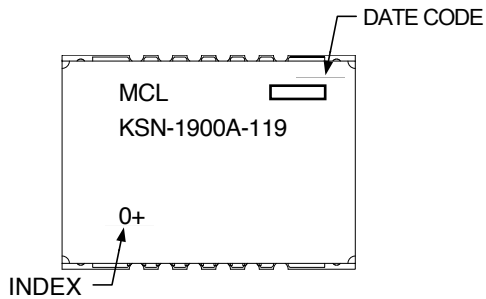


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## Device Marking



### Additional Detailed Technical Information

Additional information is available on our web site. To access this information enter the model number on our web site home page.

**Case Style:** DK1042

**Tape & Reel:** TR-F28

**Suggested Layout for PCB Design:** PL-249

**Evaluation Board:** TB-567-2+

**Environment Ratings:** ENV03T2



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